

ITJ

**CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8)**

Applicant(s): Martin et al.

Docket No.

BUR920040053US1

Application No.

10/711,742

Filing Date

10/01/2004

Examiner

Lindsay Jr, Walter Lee

Customer No.

30449

Group Art Unit

2812

Invention: GATE STACKS



I hereby certify that this Response to Notice of Drawing Inconsistency with Specification (2 pages)

(Identify type of correspondence)

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**MAIL STOP ISSUE FEES**

addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

9/27/2006

(Date)

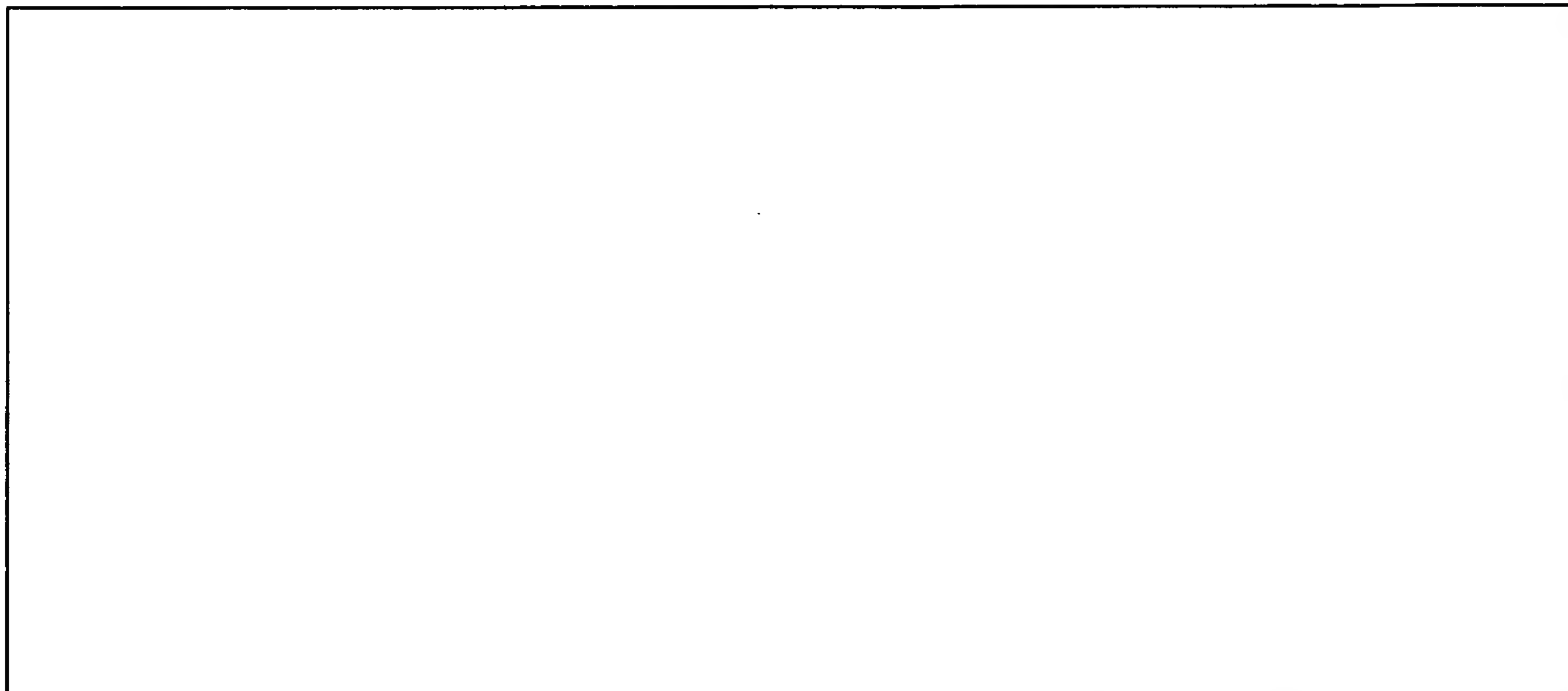
Kim Dwileski

(Typed or Printed Name of Person Mailing Correspondence)

A handwritten signature in black ink that reads "Kim Dwileski".

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Martin *et al.*

Examiner: Lindsay Jr, Walter Lee

Serial No.: 10/711,742

Art Unit: 2812

Filed: 10/01/2004

DOCKET NO.: BUR920040053US1

Title: **GATE STACKS**

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO NOTICE OF DRAWING INCONSISTENCY WITH SPECIFICATION**

This RESPONSE is in response to the Notice of Drawing Inconsistency With  
Specification mailed September 7, 2006.

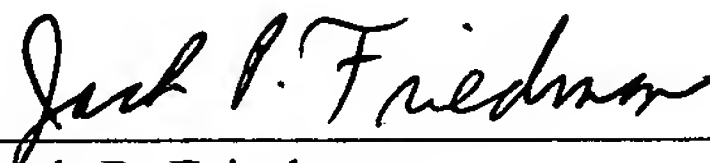
**In the Specification:**

In the Notice of Drawing Inconsistency With Specification, the Examiner stated that FIGs. 1Di, 1Dii, and 1E are in the drawings but are not listed in the Brief Description of the Drawings section of the specification. Accordingly, please amend paragraph 13 of the specification as follows.

~~FIG. 1A-1C~~ FIGs. 1A-1E illustrate cross-sectional views of a semiconductor structure after each of a series of fabrication steps is performed, in accordance with embodiments of the present invention.

The Director is hereby authorized to charge and/or credit Deposit Account 09-0456.

Date: 09/27/2006

  
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